

ARYABHATTA KNOWLEDGE UNIVERSITY

3RD year B-Tech Annual Examination, 2013

Name : DIVYENDU DIXIT **Roll No.** :105133102001
Father's Name : OM PRAKASH DIXIT **Registration No.** :10105102107
Mother's Name : MADHURI DIXIT **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	19	21	40.00	90
105318	Compiler Design	17	21	38.00	
105301	Formal Language & Automata Theory	15	21	36.00	
105314	Operating System	30	22	52.00	
105312	Database Management System	09	20	29.00	89
105339	OOPS using C++	55	20	75.00	92
105315	Computer Network	28	20	48.00	46
103301	Introduction to Microprocessor	25	19	44.00	44

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	362.00	723.00/1200
Sessional Component	400	240	361.00	
FAIL IN 105302, 105318, 105301, 105312				

NA: Not Applicable, AB: Absent, LE: Lateral Entry

ARYABHATTA KNOWLEDGE UNIVERSITY

3RD year B-Tech Annual Examination, 2013

Name : NIRAJ ANAND **Roll No.** :105133102002
Father's Name : PRABHAT KUMAR JHA **Registration No.** :10105102108
Mother's Name : LATA JHA **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	53	29	82.00	98
105318	Compiler Design	26	29	55.00	
105301	Formal Language & Automata Theory	40	29	69.00	
105314	Operating System	49	29	78.00	
105312	Database Management System	38	30	68.00	99
105339	OOPS using C++	62	30	92.00	97
105315	Computer Network	45	30	75.00	48
103301	Introduction to Microprocessor	45	29	74.00	49

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	593.00	984.00/1200
Sessional Component	400	240	391.00	
1st Div(Dist.)				

NA: Not Applicable, AB: Absent, LE: Lateral Entry

ARYABHATTA KNOWLEDGE UNIVERSITY

3RD year B-Tech Annual Examination, 2013

Name : RASHMI RANJAN **Roll No.** :105133102003
Father's Name : RANJAN KUMAR MISHRA **Registration No.** :10105102109
Mother's Name : RUPAM MISHRA **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	58	30	88.00	99
105318	Compiler Design	46	30	76.00	
105301	Formal Language & Automata Theory	49	30	79.00	
105314	Operating System	61	30	91.00	
105312	Database Management System	49	30	79.00	99
105339	OOPS using C++	67	30	97.00	98
105315	Computer Network	60	30	90.00	49
103301	Introduction to Microprocessor	51	30	81.00	49

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	681.00	1075.00/1200
Sessional Component	400	240	394.00	
1st Div(Dist.)				

NA: Not Applicable, AB: Absent, LE: Lateral Entry

ARYABHATTA KNOWLEDGE UNIVERSITY

3RD year B-Tech Annual Examination, 2013

Name : KAUSHAL KUMAR SINGH **Roll No.** :105133102004
Father's Name : CHANDRANAND SINGH **Registration No.** :10105102110
Mother's Name : SUNITA SINGH **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	25	24	49.00	92
105318	Compiler Design	26	25	51.00	
105301	Formal Language & Automata Theory	30	24	54.00	
105314	Operating System	43	24	67.00	
105312	Database Management System	44	25	69.00	91
105339	OOPS using C++	48	27	75.00	97
105315	Computer Network	53	25	78.00	48
103301	Introduction to Microprocessor	34	28	62.00	47

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	505.00	880.00/1200
Sessional Component	400	240	375.00	
1st Div(Dist.)				

NA: Not Applicable, AB: Absent, LE: Lateral Entry

ARYABHATTA KNOWLEDGE UNIVERSITY

3RD year B-Tech Annual Examination, 2013

Name : SATYAM BHARTI **Roll No.** :105133102005
Father's Name : DEEN DAYAL DIWAKAR **Registration No.** :10105102111
Mother's Name : VINU BHARTI DIWAKAR **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	18	14	32.00	77
105318	Compiler Design	26	13	39.00	
105301	Formal Language & Automata Theory	25	15	40.00	
105314	Operating System	35	15	50.00	
105312	Database Management System	20	14	34.00	76
105339	OOPS using C++	08	15	23.00	90
105315	Computer Network	41	13	54.00	44
103301	Introduction to Microprocessor	10	17	27.00	41

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	299.00	627.00/1200
Sessional Component	400	240	328.00	

FAIL IN 105302, 105312, 105339, 103301

NA: Not Applicable, AB: Absent, LE: Lateral Entry

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3RD year B-Tech Annual Examination, 2013

Name : AMIT ANUPAM **Roll No.** :105133102006
Father's Name : MITHILESH KUMAR GUPTA **Registration No.** :10105102112
Mother's Name : URMILA DEVI **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	26	15	41.00	81
105318	Compiler Design	00	15	15.00	
105301	Formal Language & Automata Theory	08	14	22.00	
105314	Operating System	20	13	33.00	
105312	Database Management System	00	14	14.00	80
105339	OOPS using C++	08	16	24.00	91
105315	Computer Network	25	14	39.00	44
103301	Introduction to Microprocessor	11	16	27.00	40

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	215.00	551.00/1200
Sessional Component	400	240	336.00	
FAIL IN 105318, 105301, 105314, 105312, 105339, 103301				

NA: Not Applicable, AB: Absent, LE: Lateral Entry

ARYABHATTA KNOWLEDGE UNIVERSITY

3RD year B-Tech Annual Examination, 2013

Name : GAURAV KUMAR **Roll No.** :105133102007
Father's Name : AVANIND SINGH **Registration No.** :10105102113
Mother's Name : SANJU DEVI **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	27	25	52.00	91
105318	Compiler Design	34	24	58.00	
105301	Formal Language & Automata Theory	26	24	50.00	
105314	Operating System	41	25	66.00	
105312	Database Management System	25	25	50.00	90
105339	OOPS using C++	55	23	78.00	95
105315	Computer Network	43	23	66.00	46
103301	Introduction to Microprocessor	27	22	49.00	45

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	469.00	836.00/1200
Sessional Component	400	240	367.00	
1st Div				

NA: Not Applicable, AB: Absent, LE: Lateral Entry

ARYABHATTA KNOWLEDGE UNIVERSITY

3RD year B-Tech Annual Examination, 2013

Name : PRITI KUMARI **Roll No.** :105133102008
Father's Name : MITHILESH KUMAR SINGH **Registration No.** :10105102114
Mother's Name : ARCHANA SINGH **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	34	26	60.00	92
105318	Compiler Design	38	27	65.00	
105301	Formal Language & Automata Theory	27	28	55.00	
105314	Operating System	47	25	72.00	
105312	Database Management System	36	27	63.00	91
105339	OOPS using C++	51	27	78.00	95
105315	Computer Network	41	27	68.00	47
103301	Introduction to Microprocessor	38	30	68.00	47

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	529.00	901.00/1200
Sessional Component	400	240	372.00	
1st Div(Dist.)				

NA: Not Applicable, AB: Absent, LE: Lateral Entry

ARYABHATTA KNOWLEDGE UNIVERSITY

3RD year B-Tech Annual Examination, 2013

Name : NEHA KUMARI **Roll No.** :105133102009
Father's Name : ARVIND CHOUDHARY **Registration No.** :10105102115
Mother's Name : RENU DEVI **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	34	25	59.00	88
105318	Compiler Design	28	26	54.00	
105301	Formal Language & Automata Theory	25	26	51.00	
105314	Operating System	40	25	65.00	
105312	Database Management System	37	25	62.00	92
105339	OOPS using C++	31	24	55.00	92
105315	Computer Network	27	25	52.00	45
103301	Introduction to Microprocessor	26	23	49.00	47

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	447.00	811.00/1200
Sessional Component	400	240	364.00	
1st Div				

NA: Not Applicable, AB: Absent, LE: Lateral Entry

ARYABHATTA KNOWLEDGE UNIVERSITY

3RD year B-Tech Annual Examination, 2013

Name : VIDYA KUMARI **Roll No.** :105133102010
Father's Name : AWADH BIHARI SINGH **Registration No.** :10105102116
Mother's Name : SUMANTI SINGH **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	52	27	79.00	97
105318	Compiler Design	46	29	75.00	
105301	Formal Language & Automata Theory	32	29	61.00	
105314	Operating System	56	29	85.00	
105312	Database Management System	55	28	83.00	98
105339	OOPS using C++	44	29	73.00	94
105315	Computer Network	51	28	79.00	47
103301	Introduction to Microprocessor	46	29	75.00	48

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	610.00	994.00/1200
Sessional Component	400	240	384.00	
1st Div(Dist.)				

NA: Not Applicable, AB: Absent, LE: Lateral Entry

ARYABHATTA KNOWLEDGE UNIVERSITY

3RD year B-Tech Annual Examination, 2013

Name : SMITA KUMARI **Roll No.** :105133102011
Father's Name : MAYANAND PASWAN **Registration No.** :10105102117
Mother's Name : LAXMI DEVI **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	55	30	85.00	98
105318	Compiler Design	42	29	71.00	
105301	Formal Language & Automata Theory	42	29	71.00	
105314	Operating System	56	29	85.00	
105312	Database Management System	49	29	78.00	97
105339	OOPS using C++	66	29	95.00	94
105315	Computer Network	54	30	84.00	47
103301	Introduction to Microprocessor	46	27	73.00	49

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	642.00	1027.00/1200
Sessional Component	400	240	385.00	
1st Div(Dist.)				

NA: Not Applicable, AB: Absent, LE: Lateral Entry

ARYABHATTA KNOWLEDGE UNIVERSITY

3RD year B-Tech Annual Examination, 2013

Name : ASHUTOSH CHANDRA MISHRA **Roll No.** :105133102012
Father's Name : SUNIL CHANDRA MISHRA **Registration No.** :10105102119
Mother's Name : MADHURI MISHRA **Exam (Month/Year)** :JUNE/2013
College/Institute : 102-VIDYA VIHAR INSTITUTE OF TECHNOLOGY, PURNEA
Branch/Course : 105-COMPUTER SCIENCE ENGG.(CSE)

Paper Code	Subject Name	Theory	Periodical	Total	Sessional
Full Marks		70	30	100	100/50
Pass Marks		25		35	60/30
105302	Design Analysis & Algorithm	33	28	61.00	99
105318	Compiler Design	42	29	71.00	
105301	Formal Language & Automata Theory	26	30	56.00	
105314	Operating System	52	29	81.00	
105312	Database Management System	44	30	74.00	98
105339	OOPS using C++	57	30	87.00	97
105315	Computer Network	48	29	77.00	47
103301	Introduction to Microprocessor	38	30	68.00	48

	Full Marks	Pass Marks	Marks Obtained	Aggregate Marks
Theory Component	800	280	575.00	964.00/1200
Sessional Component	400	240	389.00	
1st Div(Dist.)				

NA: Not Applicable, AB: Absent, LE: Lateral Entry